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PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Makoto KUDO et al.

Group Art Unit: 2184

Application No.: 10/724,885

Filed: December 2, 2003

Docket No.: 104822.01

For: MICROCOMPUTER, ELECTRONIC EQUIPMENT AND DEBUGGING SYSTEM

REPLACEMENT OF FORM PTO-1449 FILED MAY 2, 2005

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Further to the Information Disclosure Statement with Form PTO-1449 filed May 2, 2005, enclosed is a Replacement Form PTO-1449 to correct a typographical error. Specifically, the Form PTO-1449 is amended to properly indicate the pages of the disclosed Yoshida reference. Therefore, "pp. 204-247" is replaced with --pp. 240-247--.

Respectfully submitted,

James A. Oliff
Registration No. 27,075

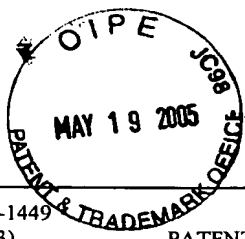
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Date: May 19, 2005

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Form PTO-1449 (REV. 8-83) US Dept. of Commerce PATENT & TRADEMARK OFFICE INFORMATION DISCLOSURE STATEMENT (Use several sheets if necessary)		ATTY DOCKET NO. 104822.01	APPLICATION NO. 10/724,885			
		APPLICANT Makoto KUDO et al.				
		FILING DATE December 2, 2003	GROUP 2184			
U.S. PATENT DOCUMENTS						
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS
FOREIGN PATENT DOCUMENTS						
		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS
	1	JP A 01-287752 (w/Abstract)	11/20/1989	Japan		
	2	JP A 02-264339 (w/Abstract)	10/29/1990	Japan		
	3	JP A 06-103104 (w/Abstract and English-language translation)	04/15/1994	Japan		
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)						
	4	Isao Yoshida, "Function and Application of Serial Port", Transistor Gijutsu, Vol. 32, No. 10, October 1, 1995, pp. 240-247 (w/partial English language translation of article)				
EXAMINER				DATE CONSIDERED		
Examiner: Initial if citation considered, whether or not citation is in conformance with M.P.E.P. 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.						

Date: May 19, 2005